DEEPA

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Faculty ID: SITF0701

Education

	Degree	Year	Institute	Specialization
1	Ph.D.	Pursuing	BMSCE, Bengaluru	Thin films
2	M.Tech.	2015	JSSATE, Noida, UP	VLSI Design and Embedded Systems
3	B.E	2002	STJIT, Ranebennur, Karnataka	Electronics and Communication Engineering
4	12^{th}	1998	JNVC Ballary, Karnataka	Science
5	10^{th}	1996	JNVC Ballary, Karnataka	General

Professional Experience

	Date (from-to)	Designation	Organization
1	20-07-2015 to till date	Assistant Professor	Department of Electronics and Instrumentation Engineering, Siddaganga Institute of Technology, Tumakuru-572103, Karnataka, India

Positions held

- *Coordinated departmental level works as mentioned below:*

- Department level Internship/Industrial Training (UG/PG)
- Department level mini project (UG)
- Timetable coordinator
- Department level Website Coordinator
- Department Digital System Design Laboratory Coordinator (UG)
- Department Digital Electronics Laboratory Coordinator (UG)

- Department VLSI Design Laboratory Coordinator (PG)
- Various other work allotted at Department and Institute level.

Affiliations of Professional organizations

- NIL

Awards and Honors

- NPTEL course CMOS Digital VLSI Design Feb-April 2019 **Topper and silver badge** with consolidated score of 83%.
- Online Programme conducted by IIT, Bombay Use of ICT in Education for Online and Blended Learning May 2nd - July 10th, 2016 Awarded as one of the top performers

Courses Taught

Undergraduate Courses

- CMOS Digital VLSI Design
- Computer Networks
- Network Analysis
- Control system
- Digital System Design using HDL
- Analog Electronic Circuits,

Postgraduate Courses

- CMOS Digital VLSI Design
- VLSI Testing and Verification

Research Guidance

Sl. no	Name of the Scholar	Title	Year of completion
1.	NIL	NIL	NIL

Research Areas

- VLSI Design
- Thin Film Technology
- Embedded Systems

Sponsored Projects

Ongoing Projects: NIL
Completed Projects: NIL

Publications

Journals

- **Deepa**, M. Anantha Sunil, Sabina Rahaman, K.B. Jagannatha, Kilari Naveen Kumar (2025), “Chalcogenide p-type absorber layer studies of spray pyrolyzed CuZnS thin films for heterojunction solar cell applications”, Optical Materials, Volume 164,2025,117036, ISSN 0925-3467, https://doi.org/10.1016/j.optmat.2025.117036.
- **Deepa**, **(2017),** “Open Education Resource:An effective ICT tool for Engineering Education”,**Journal Engineering Education Transformations(JEET)** , Volume 31 , No. 1, July 2017, ISSN 2349-2473, eISSN 2394-1707.
- Ajay P.C , Satyam Satyarthi and **Deepa** (2020), “Smart System For Human Detection And Tracking During Natural Calamities “, JI of the Instrument Soc. Of India, December 2020.
- Arun Kumara and **Deepa,** (2019), “Modulation Signal Chain for a 5G PDSCH Receiver”, Computer Reviews Journal Vol 5 (2019) ISSN: 2581-6640.
- Mahesh E, Sujeeth Subramanya and **Deepa**, (2018) “Monitoring and Controlling of Digital Energy Meter through IoT”, JI of the Instrument Soc. Of India.

Conference Proceedings

- A. Sadath and **Deepa** (2023), "Design and Implementation of Low Power-Novel Encoder Based Flash ADC," *2023 International Conference on Smart Systems for applications in Electrical Sciences (ICSSES)*, Tumakuru, India, 2023, pp. 1-6, doi: 10.1109/ICSSES58299.2023.10200521.
- A. Sadath and **Deepa** (2022), "Design of a Novel Encoder for Flash Analog to Digital Converter," *2022 IEEE International Conference on Data Science and Information System (ICDSIS)*, Hassan, India, 2022, pp. 1-6, doi: 10.1109/ICDSIS55133.2022.9915797.
- S. P. Surabhi and **Deepa** (2022), "Design and Analysis of Low Power High Gain Amplifiers for DAC Application," *2022 IEEE International Conference on Data Science and Information System (ICDSIS)*, Hassan, India, 2022, pp. 1-6, doi: 10.1109/ICDSIS55133.2022.9915853.
- M. A. Yadhav and **Deepa** (2021), "Design and Implementation of Low Power and High- Speed Full Adder," 2021 International Conference on Recent Trends on Electronics, Information, Communication & Technology (RTEICT), 2021, pp. 743-748, doi: 10.1109/RTEICT52294.2021.9574023.
- S. Srilakshmipathy, R. Abhishek and **Deepa** (2021), "Temperature and Humidity Monitoring in Silo," 2021 International Conference on Recent Trends on Electronics, Information, Communication & Technology (RTEICT), 2021, pp. 55-59, doi: 10.1109/RTEICT52294.2021.9573918.

- Amitha.M and **Deepa** (2021), "Comparison between CMOS full adder and PTL full adder" IOP Conf. Series: Materials Science and Engineering, doi:10.1088/1757-899X/1065/1/012047

- Rakshitha A and **Deepa** (2018), "Performance analysis of CMOS Inverter with power reduction Techniques", Proceedings of the International Conference on Inventive Computation Technologies (ICICT-2018), ISBN:978-1-5386-4984-8.

- Girish N.P and **Deepa**, (2018) "Designing HMI using Rx series Rx63N Microcontroller", IEEE International Conference On Recent Trends In Electronics Information Communication Technology(RTEICT), May 19-20, 2017, India. pp1049 – 1053.

- **Deepa** and Sampath Kumar V, (2015) "Analysis of Energy Efficient PTL based Full Adders using different Nanometer Technologies", Proceedings of IEEE International Conference on Electronics and Communication Systems(ICECS2015), Feb 2015, PP 310 – 315.
- **Deepa** and Sampath Kuamr V, (2015) "Analysis of low Power 1-bit Adder Cells using different xor- xnor gate", Proceedings of IEEE International Conference on Computational Intelligence and Communication Technology (ICICT 2015), Feb 2015, PP 488 – 492.

Book Chapters

- NIL

Books

- NIL

Editorial

- NIL

Reviewer of Journals

- NIL

Editor/ Reviewer of Journal

- NIL

Patents

- NIL