

ACADEMIC YEAR: 2025-26

**SIDDAGANGA INSTITUTE OF TECHNOLOGY,
TUMAKURU 572103, KARNATAKA, INDIA**

**DEPARTMENT OF ELECTRONICS AND
INSTRUMENTATION ENGINEERING**



SCHEME OF STUDY

FOR

PG Course in

VLSI Design and Embedded Systems

2025-26

SIDDAGANGA INSTITUTE OF TECHNOLOGY, TUMAKURU-03
(An autonomous Institute under Visvesvaraya Technological University)

SCHEME OF TEACHING AND EXAMINATION
I SEMESTER M.Tech. VLSI Design and Embedded Systems

I SEMESTER												
	Course Type BSC/PCC / IPCC /PCC(PB) /	Course Code	Course Title	Teaching Hours per Week			Examination				Credits	
				Theory	Practical/ Seminar	Tutorial/ SDA	Duration in hours	CIE Marks	SEE Marks	Total Marks		
				L	P	T/SDA						
1	IPCC	S1LVSI01	Digital System Design using Verilog HDL	3	2	0	03	50	50	100	4	
2	PCC(PB)	S1LVS02	VLSI Design	3	0	0	03	50	50	100	3	
3	PCC	S1LVS03	ARM Microcontroller and its Applications	3	0	0	03	50	50	100	3	
4	PCC	S1LVS04	Analog IC Design	3	0	0	03	50	50	100	3	
5	PEC	S1LVSE1x	Professional Elective - I	3	0	0	03	50	50	100	3	
6	PEC	S1LVSE2x	Professional Elective - II	3	0	0	03	50	50	100	3	
7	PCCL	S1LVSL1	Embedded Systems Lab	0	4	0	03	50	50	100	2	
8	NMC	S1PGRM	Research Methodology and IPR	VTU Online course MRMI19								
Total				18	6	0	21	350	350	700	21	
<p>Note: BSC-Basic Science Courses, PCC: Professional core. IPCC-Integrated Professional Core Courses, PCC(PB): Professional Core Courses (Project Based), PCCL-Professional Core Course lab, NMC- None Credit Mandatory Course, L-Lecture, P-Practical, T/SDA-Tutorial / Skill Development Activities (Hours are for Interaction between faculty and students) MRMI19- Research Methodology and IPR (Online) for the students who have not studied this course in the Undergraduate level. This course is not counted for vertical progression, Students have to qualify for the award of the master's degree.</p>												
<p>M- Master program xx – ME for Mechanical Engineering Stream, CV for Civil Engineering Stream, EE – Electrical & Electronics Engineering Stream, EC- Electronics and Communication Engineering Stream, CS- Computer Science and Engineering BA - Business Administration AR- Architecture- etc.</p>												

BSC: Basic Science Courses: Courses like Mathematics/ Science are the prerequisite courses that the concerned engineering stream board of Studies will decide. **PCC: Professional Core Course:** Courses related to the stream of engineering, which will have both CIE and SEE components, students have to qualify in the course for the award of the degree. **Integrated Professional Core Course (IPCC):** Refers to a Professional Theory Core Course Integrated with practicals of the same course. The IPCC's theory part shall be evaluated by CIE and SEE. The practical part shall be evaluated by only CIE (no SEE). However, questions from the practical part of IPCC shall be included in the SEE question paper. **Project Based Learning Course (PCC(PB):** Project Based Learning course is a professional core Course only Students have to complete a project out of learning from the course and SEE will be viva voce on project work. **PCCL: Professional Core Course Laboratory:** Practical courses whose CIE will be evaluated by the class teacher and SEE will be evaluated by the two examiners.

Skill development activities: Under Skill development activities in a concerning course, the students should

1. Interact with industry (small, medium, and large).
2. Involve in research/testing/projects to understand their problems and help creative and innovative methods to solve the problem.
3. Involve in case studies and field visits/ fieldwork.
4. Accustomed to the use of standards/codes etc., to narrow the gap between academia and industry.
5. Handle advanced instruments to enhance technical talent.
6. Gain confidence in the modelling of systems and algorithms for transient and steady-state operations, thermal study, etc.
7. Work on different software/s (tools) to simulate, analyze and authenticate the output to interpret and conclude.

All activities should enhance student's abilities to employment and/or self-employment opportunities, management skills, Statistical analysis, fiscal expertise, etc. Students and the course instructor/s are to be involved either individually or in groups to interact together to enhance the learning and application skills of the study they have undertaken. The students with the help of the course teacher can take up relevant technical –activities that will enhance their skills. The prepared report shall be evaluated for CIE marks.

MRMI19-Research Methodology and IPR- Non Credit Mandatory Course (NMC) if students have not studied this course in their undergraduate program then he /she has to take this course at <http://online.vtu.ac.in> and to qualify for this course is compulsory before completion of the minimum duration of the program (Two years), however, this course will not be considered for vertical progression.

Professional Elective - I		Professional Elective - II	
Course Code	Course Title	Course Code	Course Title
S1LVSE11	ASIC Design	S1LVSE21	Data Structures
S1LVSE12	VLSI Design Automation	S1LVSE22	Low power VLSI Design
S1LVSE13	VLSI for Digital Signal Processing	S1LVSE23	VLSI Testing and Verification

SIDDAGANGA INSTITUTE OF TECHNOLOGY, TUMAKURU-03
(An autonomous Institute under Visvesvaraya Technological University)

SCHEME OF TEACHING AND EXAMINATION
II SEMESTER M.Tech. VLSI Design and Embedded Systems

II SEMESTER												
			Course Code	Course Title	Teaching Hours /Week			Examination				Credits
					Theory	Practical/ Seminar	Tutorial/ Skill Development Activities	Duration in hours	CIE Marks	SEE Marks	Total Marks	
					L	P	T/SDA					
1	IPCC	PCC/ PEC/ MDC/ PCC (PB) /IPCC	S2LVSI01	Real time operating systems	3	2	0	03	50	50	100	4
2	PCC		S2LVS02	Semiconductor Process Technology	3	0	0	03	50	50	100	3
3	PCC(PB)		S2LVS03	Design of IoT Systems	3	0	0	03	50	50	100	3
4	PEC		S2LVSE3x	Professional Elective – III	3	0	0	03	50	50	100	3
5	PEC		S2LVSE4x	Professional Elective – IV	3	0	0	03	50	50	100	3
6	PCCL	PCC L	S2LVSL1	VLSI Design Lab	0	4	0	03	50	50	100	2
7		AEC/ SEC	S2LVSA20x	Ability/Skill Enhancement Course (Offline/Online)	00	02	---	03	50	50	100	1
					01	00	---					
TOTAL					16	08	0	21	350	350	700	19

Note: **PCC**: Professional core. **IPCC**-Integrated Professional Core Courses, **PCC(PB)**: Professional Core Courses (Project Based), **PCCL**-Professional Core Course lab, **PEC**- Professional Elective Courses, **MDC**- Multi-Disciplinary Courses, **L-Lecture, P-Practical, T/SDA-Tutorial / Skill Development Activities** (Hours

are for Interaction between faculty and students) **L-Lecture, P-Practical, T/SDA-Tutorial / Skill Development Activities** (Hours are for Interaction between faculty and students) **PBLC**: Project Based Learning Course,

Note: **xxx** means specialization code for example **MDE- Design** Engineering, **LDN-** Digital Communication and Networking, **SCE-** Computer Engineering, **CCT-** Construction Technology, **AUD-** Urban Design, **MBA-** Master of Business Administration, **MCA-**Master of Computer Application, etc.

Ability Enhancement Courses (AEC):. These courses are designed to help students enhance their skills in communication, language, and personality development. They also promote a deeper understanding of subjects like social sciences and ethics, culture and human behaviour, human rights, and the law. Skill Enhancement Course (SEC): Skill Enhancement Course means a course designed to provide value-based or skill-based knowledge and should contain both theory and lab/hands-on/training/fieldwork. The main purpose of these courses is to provide students with life skills in the hands-on mode to increase their employability. If AEC/SEC courses are ONLINE (MOOCs) courses suggested by the concerned board of studies. These courses will be made available on www.online.vtu.ac.in, however online courses are not considered for vertical progression, but qualifying in online courses is mandatory for the award of the degree.

Professional Elective - III		Professional Elective - IV	
Course Code	Course Title	Course Code	Course Title
S2LVSE31	CMOS RF Circuit Design	S2LVSE41	System on Chip Design
S2LVSE32	Mixed Signal Circuit Design	S2LVSE42	System Verilog
S2LVSE33	Nano Electronics	S2LVSE43	RISC-V Processor Design
S2LVSE34	Design of CMOS Phase Locked Loops	S2LVSE44	Static Timing Analysis
S2LVSE35	Hardware Security Systems		

Ability / Skill Enhancement Courses				
Course Code	Course Title	L	P	T/SDA
S2LVSA201	Peripheral and IO Firmware development	0	2	0
S2LVSA202	Machine learning for Embedded Systems	0	2	0
S2LVSA203	Data analytics using R-programming.	0	2	0

PCC/PCCL/IPCC/PEC/MDC/PCC (PB): These are the courses which will suit the individual specializations

SIDDAGANGA INSTITUTE OF TECHNOLOGY, TUMAKURU-03
(An autonomous Institute under Visvesvaraya Technological University)

SCHEME OF TEACHING AND EXAMINATION

III SEMESTER M.Tech. VLSI Design and Embedded Systems

**For the students who are willing to take up a two-semester duration Industry/Research Internship
Leading to Project work /start-up**

III SEMESTER (A)											
Sl. No	Course	Course Code	Course Title	Teaching Hours /Week			Examination				Credits
				Theory	Practical/ Mini- Project/ Internship	Tutorial/ Skill Development Activities	Duration in hours	CIE Marks	SEE Marks	Total Marks	
				L	P	SDA					
1	PEC/ MDC	S3LVS301/401	(Online Courses) 12 weeks duration							100	3
2		S3LVS302/402	(Online Courses)12 weeks duration							100	3
		S3LVS303/403	(Online Courses)12 weeks duration							100	3
3	INT	S3LVSINT	Research Internship /Industry- Internship leading to project work/ Startup	Two-semester duration, SEE in the IV semester which leads to project work /start-up			03	100	- - -	100	3
TOTAL							03	100	-	400	12

S3LVS30x/40x	(Online Courses) 12 weeks duration
S3LVS301/401	Digital VLSI Testing
S3LVS302/402	VLSI Design flow RTL to GDS
S3LVS303/403	Introduction to Industry 4.0 and Industrial Internet of Things
S3LVS304/404	Computer Architecture
S3LVS305/405	Multi core Computer Architecture
S3LVS306/406	C-based VLSI Design
S3LVS307/407	Fundamentals of Micro and Nanofabrication
S3LVS308/408	Introduction to Virtual Reality
S3LVS309/409	Sensors and Actuators
S3LVS310/410	Thin film Technology
S3LVS311/411	Fundamentals of Micro and Nanofabrication
S3LVS312/412	Sensor Technologies, Physics, fabrication and circuits

SIDDAGANGA INSTITUTE OF TECHNOLOGY, TUMAKURU-03
(An autonomous Institute under Visvesvaraya Technological University)
SCHEME OF TEACHING AND EXAMINATION
IV SEMESTER M.Tech. VLSI Design and Embedded Systems

IV SEMESTER (A)										
Sl. No	Course	Course Code	Course Title	Teaching Hours		Examination				Credits
				Theory	Practical/ Field work	Duration in hours	CIE Marks	SEE Marks Viva voce	Total Marks	
1	INT	S4INT401	Research Internship / Industry Internship Leading to Project Work/Start-up	Two Semester Duration		03	100	100	200	12
2	PROJ	SPROJ402	Project			03	100	100	200	16
TOTAL						06	200	200	400	28

INT: Industry/ Research Internship leading to the project work /startup **PROJ:** Project work outcome of Internship (Project Phase-II is Viva voce SEE) Taking up a two-semester Industry/Research Internship that leads to project work or a start-up can be a highly rewarding experience for students. It allows them to apply theoretical knowledge in practical settings, gain valuable industry or research experience, and potentially develop innovative solutions or business ideas. Here are some key steps and considerations for students pursuing such an internship:Industry Internship: The main objective of the industry internship is to ensure that the intern is exposed to a real-world environment and gain practical experience. Often, it may be a practical exposure to the theory that has been learned during the academic period. The industry internship helps students understand of analytical concepts and tools, hone their skills in real-life situations, and build confidence in applying the skills learned.

Research Internship: A research internship is an opportunity for students or early career professionals to gain hands-on experience in conducting research under the guidance of a mentor or within a research team. These internships can take place in academic institutions, research organizations, government agencies, or private companies

Research /Industry Internship: In the third-semester Students have to be in touch with a guide/mentor/coordinator and regularly submit the report referred to the progress internship. Based on the progress report the Guide/Mentor/coordinator has to enter the CIE marks at the end of the 3rd semester. At the beginning of the 4th semester, students have to define the project topic out of the learning due to the Internship, upon completion of the project work he/she has to attend the SEE at the parent Institute.

Internship Leading to Start-up: An internship that leads to a startup is an exciting pathway, blending real-world experience with entrepreneurial ambition. Here's a comprehensive guide to transitioning an internship experience into launching your startup: 1) Maximize your internship experience, 2) Identifying Viable Business Ideas, 3) Research and Validation 4) Building a Business Plan 5) Networking and Mentorship 6) Securing Funding 7) Establishing Startup 8) Launching and Marketing. By following these steps, you can effectively transition from an internship to launching a successful startup. This journey requires dedication, resilience, and a willingness to learn and adapt.

Mxxx301/401 to 303/403: MOOC courses of 12 weeks duration are the courses suggested by the Board of Studies of the University and will be displayed on www.online.vtu.ac.in. The online courses selected should not be the same as those studied in the first and second semesters of the program. The student will not be eligible to get their degree if they unintentionally select online courses that match previously finished courses. These courses are not considered for the vertical progression; however, qualifying for these courses and earning the credits is a must for the award of the degree. It is permitted to complete these online MOOC courses either in 3rd semester or in 4th semester.

SIDDAGANGA INSTITUTE OF TECHNOLOGY, TUMAKURU-03
 (An autonomous Institute under Visvesvaraya Technological University)
SCHEME OF TEACHING AND EXAMINATION
III SEMESTER M.Tech. VLSI Design and Embedded Systems

For the students who are willing to take an Industry Internship for one-semester duration and independent project work next semester											
III SEMESTER (B)											
	Course	Course Code	Course Title	Teaching Hours /Week			Examination				Credits
				Theory	Practical/ Mini- Project/ Internship	Tutorial/ Skill Developmen t Activities	Durati on in hours	CIE Marks	SEE Marks	Total Marks	
				L	P	SDA					
1	MDC/ PEC	S3LVS301/401	(Online Course) (12 weeks courses)							100	3
		S3LVS302/402	(Online Course) (12 weeks courses)							100	3
2		S3LVS303/403	(Online Courses) (12- week course)							100	3
3	INT	S3LVSINT	Industry Internship	One semester Duration			03	100	100	200	11
TOTAL				00	00	00	03	100	100	500	20

SIDDAGANGA INSTITUTE OF TECHNOLOGY, TUMAKURU-03
(An autonomous Institute under Visvesvaraya Technological University)
SCHEME OF TEACHING AND EXAMINATION
IV SEMESTER M.Tech. VLSI Design and Embedded Systems

IV SEMESTER (B)										
	Course	Course Code	Course Title	Teaching Hours /Week		Examination				Credits
				Theory	Practical/ Field work	Duration in hours	CIE Marks	SEE Marks Viva voce	Total Marks	
				L	P					
1	Project	S4LVSPR	Project work	--	08	03	100	100	200	20
TOTAL				00	08	03	100	100	200	20
<p>Industry Internship: The main objective of the industry internship is to ensure that the intern is exposed to a real-world environment and gains practical experience. Often, it may be a practical exposure to the theory that has been learned during the academic period. The industry internship helps students understand of analytical concepts and tools, hone their skills in real-life situations, and build confidence in applying the skills learned. The students who take up a one-semester Internship in the Industry have to appear SEE at the institute at the end of the semester as per the examination calendar.</p> <p>Project Work: Students in consultation with the guide shall carry out literature survey/ visit industries to finalize the topic of the Project. Subsequently, the students shall collect the material required for the selected project, prepare a synopsis, and narrate the methodology to carry out the project work. Each student, under the guidance of a Faculty, is required to</p> <ul style="list-style-type: none"> · Present the seminar on the selected project orally and/or through Power Point slides. · Answer the queries and be involved in debate/discussion. · Submit two copies of the typed report with a list of references. · The participants shall take part in discussions to foster a friendly and stimulating environment in which the students are motivated to reach high standards and become self-confident <p>CIE marks for the project report (20 marks), seminar (20 marks) and question and answer (10 marks) shall be awarded (based on the quality of report and presentation skill, participation in the question and answer session by the student) by the committee constituted for the purpose by the Principal. The committee shall consist of internal guide and a faculty from the department with the senior most acting as the Chairperson.</p> <p>Semester End Examination SEE marks for the project report (30 marks), seminar (10 marks) and question and answer session (10 marks) shall be awarded (based on the quality of the report and presentation skill, participation in the question and answer session) by the</p>										

examiners appointed by the University.

MOOC301/401 to 303/403: MOOC courses of 12 weeks duration are the courses suggested by the Board of Studies of the University and will be displayed on www.online.vtu.ac.in. The online courses selected should not be the same as those studied in the first and second semesters of the program. The student will not be eligible to get their degree if they unintentionally select online courses that match previously finished courses. These courses are not considered for the vertical progression; However, qualifying for these courses and earning the credits is a must for the award of the degree. It is permitted to complete these online MOOC courses either in 3rd semester or in 4th semester.

SIDDAGANGA INSTITUTE OF TECHNOLOGY, TUMAKURU-03
 (An autonomous Institute under Visvesvaraya Technological University)
SCHEME OF TEACHING AND EXAMINATION
III SEMESTER M.Tech. VLSI Design and Embedded Systems

For the students who are willing to take a research-leading paper publication in Q1/Q2/Q3 Journals and to a PhD Registration											
III SEMESTER (C)											
	Course	Course Code	Course Title	Teaching Hours /Week			Examination				Credits
				Theory	Practical/ Mini- Project/ Internship	Tutorial/ Skill Development Activities	Duration in hours	CIE Marks	SEE Marks	Total Marks	
				L	P	SDA					
1	PCC/I PCC/ MDC/ PEC	S3LVS30 1/401	(Online Course) (12 weeks courses)							100	3
		S3LVS30 2/402	(Online Course) (12 weeks courses)							100	3
2		S3LVS30 3/403	(Online Courses) (12-week course)							100	3
		S3LVS30 4/404	(Online Courses) (12-week course)							100	3
3	PROJ	S3LVSPR	Project Phase-I	One semester Duration			03	100	-	100	6
TOTAL				00	00	00	03	100	-	500	18

SIDDAGANGA INSTITUTE OF TECHNOLOGY, TUMAKURU-03
(An autonomous Institute under Visvesvaraya Technological University)

SCHEME OF TEACHING AND EXAMINATION

IV SEMESTER M.Tech. VLSI Design and Embedded Systems

IV SEMESTER (C)											
Sl · N o	Course	Course Code		Course Title	Teaching Hours /Week		Examination				Credit s
					Theor y	Practical/ Field work	Duration in hours	CIE Marks	SEE Mark s Viva voce	Total Mark s	
					L	P					
1	Project	S4LVSPR		Project work	--	08	03	100	100	200	22
TOTAL					00	08	03	100	100	200	22

The research section of the university has to announce the number of seats for M.Tech. students who are seeking PhD (research study) admission through a project leading to the publication of the paper in Q1/Q2/Q3 journals. Only full-time research work will be permitted in the university department or approved research centers of the affiliated colleges of the university (guidelines need to be set up).

Based on Seat availability, the students are permitted to register for project work leading to the publication of papers in Q1/Q2/Q3 journals and admission to research (PhD) in their 3rd semester of the M.Tech., program

Project Phase-1 Project Phase-I, typically the initial phase in any project, is crucial as it lays the foundation for the entire project. This phase involves defining the project's scope, objectives, and initial planning. Here's a structured approach to effectively carry out Project Phase-I:

Project Charter: Outlines the project's purpose, objectives, and stakeholders.

Scope Statement: Defines the project boundaries and deliverables.

Requirements Document: Captures all project requirements.

Project Plan: Details the approach, timeline, and resource allocation.

Risk Management Plan: Identifies and plans for potential risks.

Feasibility Study Report: Assesses technical, economic, and operational feasibility.

Students in consultation with the guide shall carry out literature survey/ visit industries to finalize the topic of the Project. Subsequently, the students shall collect the material required for the selected project, prepare a synopsis, and narrate the methodology to carry out the project work. Each student, under the guidance of a faculty, is required to Present the seminar on the selected project orally and/or through power point slides. Answer the queries and be involved in debate/discussion. Submit two copies of the typed report with a list of references.

The participants shall take part in discussions to foster a friendly and stimulating environment in which the students are motivated to reach high standards and become self-confident.

Continuous Internal Evaluation (100 Marks).

CIE marks for the project report (60 marks), seminar (20 marks) and question and answer (20 marks) shall be awarded (based on the quality of report and presentation skill, participation in the question and answer session by the student) by the committee constituted for the purpose by the Principal. The committee shall consist of an internal guide and a faculty from the department with the senior most acting as the Chairperson.

Project Work Phase - II: Each student shall be involved in carrying out the project work jointly in constant consultation with internal guide and external guide and prepare the project report as per the norms of the university to avoid plagiarism. Phase II of a project typically involves the detailed execution of the planned activities, continuous monitoring and control of the project's progress, and making necessary adjustments to ensure the project stays on track. Keep detailed records of all project activities, decisions, and changes. Ensure all project documentation is organized and accessible. Conduct a final project review to evaluate overall performance, achievements, and lessons learned. Document best practices and areas for improvement for future projects.

Paper Publication Process: Publishing a research paper based on your project in a Q1/Q2/Q3 journal involves several key steps, from writing the manuscript to navigating the peer review process. Here's a comprehensive guide:

Writing the Manuscript: Choose a clear and concise title that accurately reflects the content. Write an abstract summarizing the research question, methods, results, and conclusions.

Literature Review: Review relevant existing research to establish the foundation of your study. Identify gaps that your research aims to fill.

Methodology: Describe the research design, methods, and procedures in detail. Include information on data collection, analysis, and any tools or software used.

Results: Present the findings of your research clearly and logically. Use tables, figures, and charts to illustrate key results.

Discussion: Interpret the results and explain their implications. Compare your findings with existing research and discuss any discrepancies or new insights.

Conclusion: Summarize the main findings and their significance. Suggest potential future research directions.

References: Cite all sources used in your research following the journal's citation style.

Journal Selection: Choose a journal that aligns with the scope and focus of your research. Consider the journal's impact factor (Q1, Q2, Q3) and audience.

Review Journal Guidelines: Carefully read the journal's submission guidelines and ensure your manuscript adheres to them.

Prepare Your Manuscript: Format your manuscript according to the journal's guidelines. Include all required sections and supplementary materials.

Cover Letter: Write a cover letter to the journal editor highlighting the significance of your research and why it fits the journal.

Submit the Manuscript: Use the journal's online submission system to submit your manuscript. Ensure all required information and documents are included.

Semester End Examination SEE marks for the project report (60 marks), seminar (20 marks) and question and answer session (20 marks) shall be awarded (based on the quality of report and presentation skill, participation in the question and answer session) by the examiners appointed by the University.