

Siddaganga Institute of Technology, Tumakuru-572103
DEPARTMENT OF ELECTRONICS & INSTRUMENTATION ENGINEERING
M.Tech, VLSI Design & Embedded systems
Paper publication 2016-2017 Batch

Sl.No	USN	Name	Title of the Paper	National/ International	Held on	Organized by
1.	1SI16LVS01	Anand K.N	Design of Battery charger with power path controller and fuel gauge for point of sale (POS) terminal	International	18 th -19 th May 2018	Sri Venkateshwara college of Engineering, Bengaluru-562157
2.	1SI16LVS02	Arpitha K	Design & implementation of power supply system for vehicle simulator	National	3 rd March 2018	Siddaganga Institute of Technology, Tumakuru-03
3.	1SI16LVS06	Deexith V	Android 7.1.2 porting on Magik2 board based on I.MX6 processor	International	10 th June 2018	Science globe in association with IRAJ research forum, Bengaluru
4.	1SI16LVS07	Harshitha G.H	Hardware Interface test launcher and shutdown & reboot feature in setting UI	International	10 th June 2018	Science globe in association with IRAJ research forum, Bengaluru
5.	1SI16LVS09	Kavya M	Verification and validation of Data on Wi-Fi over the AIR (OTA)	International	18 th -19 th May 2018	Sri Venkateshwara college of Engineering, Bengaluru-562157
6.	1SI16LVS10	Nagamani S R	Design and implementation of remote smart parking assistance in automated vehicles	National	3 rd March 2018	Siddaganga Institute of Technology, Tumakuru-03
7.	1SI16LVS11	Pavithra S	Intelligent container for ideal food transportation	National	3 rd March 2018	Siddaganga Institute of Technology, Tumakuru-03
8.	1SI16LVS13	Sahana V	Design of secure smart card reader and Wi-Fi interface for point of sale terminal	International	18 th -19 th May 2018	Sri Venkateshwara college of Engineering, Bengaluru-562157