

Ms. Deepa

Publications

List of papers published in National Journal/ International journal

1. Ajay P.C 1 , Satyam Satyarthi 2 and Deepa, “Smart System For Human Detection And Tracking During Natural Calamities “,**JI of the Instrument Soc. Of India**, December 2020.
2. Arun Kumara and **Deepa**, (2019), “Modulation Signal Chain for a 5G PDSCH Reciever”, **Computer Reviews Journal Vol 5 (2019) ISSN: 2581-6640**.
3. Mahesh E,Sujeeth Subramanya and **Deepa**,(2018) “Monitoring and Controlling of Digital Energy Meter through IoT”, **JI of the Instrument Soc. Of India**.
4. **Deepa**, (2017)“Open Education Resource:An effective ICT tool for Engineering Education”,**Journal Engineering Education Transformations(JEET)** , Volume 31 , No. 1, July 2017, ISSN 2349-2473, eISSN 2394-1707.

List of papers Presented/Published in International

1. M. A. Yadhav and **Deepa**, "Design and Implementation of Low Power and High-Speed Full Adder," 2021 **International Conference on Recent Trends on Electronics, Information, Communication & Technology (RTEICT), 2021**, pp. 743-748, doi: 10.1109/RTEICT52294.2021.9574023.
2. S. Srilakshmi pathy, R. Abhishek and **Deepa**, "Temperature and Humidity Monitoring in Silo," 2021 **International Conference on Recent Trends on Electronics, Information, Communication & Technology (RTEICT), 2021**, pp. 55-59, doi: 10.1109/RTEICT52294.2021.9573918.
3. Amitha.M , **Deepa**(16-Feb-2021), “Comparison between CMOS full adder and PTL full adder” **IOP Conf. Series: Materials Science and Engineering**, doi:10.1088/1757-899X/1065/1/012047
4. Rakshitha A and **Deepa**, (2018) “Performance analysis of CMOS Inverter with power reduction Techniques”, **Proceedings of the International Conference on Inventive Computation Technologies (ICICT-2018)**, ISBN:978-1-5386-4984-8.
5. Girish N.P and **Deepa**, (2018) “Designing HMI using Rx series Rx63N Microcontroller”, **IEEE International Conference On Recent Trends In**

- Electronics Information Communication Technology(RTEICT)**, May 19-20, 2017, India. pp1049 – 1053.
6. **Deepa** and Sampath Kuamr V, (2015) “Analysis of Energy Efficient PTL based Full Adders using different Nanometer Technologies”,**Proceedings of IEEE International Conference on Electronics and Communication Systems(ICECS2015)**, Feb 2015, PP 310 – 315.
 7. **Deepa** and Sampath Kuamr V,(2015) “Analysis of low Power 1-bit Adder Cells using different xor- xnor gate”, **Proceedings of IEEE International Conference on Computational Intelligence and Communication Technology (ICICT 2015)**, Feb 2015, PP 488 – 492.

List of papers Presented/Published in National Conference

1. Safiya Anjum, Hemalatha N.R. and **Deepa**, (2018) “ Design and implementation of subtractor using 130nm technology ”, **National Conference on Recent Trends in Electrical Sciences(NCRTES)**, March 2018,SIT,Tumakuru.
2. Lakshmi K, Arpitha M. and **Deepa**, (2018) “Design of power and speed efficient counter using 130nm technology”, **National Conference on Recent Trends in Electrical Sciences(NCRTES)**, March 2018,SIT,Tumakuru.
3. Vidya S., Chaitra M. and **Deepa**, (2018) “Design and Analysis of Low Power MTCMOS Adder in 130nm Technology”, **National Conference on Recent Trends in Electrical Sciences(NCRTES)**, March 2018,SIT,Tumakuru.
4. Sharanaiah and **Deepa**, (2018) “Analysis and comparison: full adders”, **National Conference on Recent Trends in Electrical Sciences(NCRTES)**, March 2018,SIT,Tumakuru.
5. Murthy P Halageri and **Deepa**, (2018) “Performance analysis of 1-bit ALU in 130 nm scale using CMOS logic”, **National Conference on Recent Trends in Electrical Sciences (NCRTES)**, March 2018,SIT,Tumakuru.
6. Rakshitha A, Bindu T.R. and **Deepa**, (2018) “Performance analysis of various CMOS Inverter”, **National Conference on Recent Trends in Electrical Sciences(NCRTES)**, March 2018,SIT,Tumakuru.