

<b>FACULTY NAME</b>	<b>SUBJECT</b>	<b>INNOVATIVE METHOD ADAPTED</b>
Dr.V.Siddeswara Prasad	Analog Electronic Circuits	Simulation and analysis of electronic circuit using Multi sim.
Dr. H. M. Kalpana	Digital Electronic Circuits	Simulation and analysis of electronic circuit using Multi sim.
Dr.H. K .E. Latha	ACS	MAT Lab programming was introduced for simulation studies.
		Group discussion on non linear system
	Thin film Instrumentation	Nichrome thin film deposition was demonstrated in the research lab to the students.
Mrs.D.Savitha	Industrial Control system and automation	PLC programs are simulated using demo kit.
Mrs.Rajeshwari B.S.	Integrated circuits and applications	Analysis of Op-amp circuits using MultiSim
	Electronic Measurements	Summary of chapter by students
	Internet of Things	Implementation of case studies by students
	Digital Signal Processing	Group Assignments
Mr.Kishore C.	Advanced Microcontroller	Simulation and module demonstration
	Digital VLSI Design	Simulation using Microwind tool.
Mrs.Deepa	Digital System Design (V Sem B.E)	Creation of OER: <a href="https://digitalsystemdesignblog.wordpress.com">https://digitalsystemdesignblog.wordpress.com</a>  Creation of video for Xilinx ISE(Lab): <a href="https://www.youtube.com/watch?v=J_KvoTIVefw">https://www.youtube.com/watch?v=J_KvoTIVefw</a> .  Videos for basics of digital system design:  <b>BASIC GATES by Deepa R.M.</b>

		<p>URL: <a href="https://www.youtube.com/watch?v=EqEPe99520w">https://www.youtube.com/watch?v=EqEPe99520w</a></p> <p>UNIVERSAL GATES by Deepa R.M.</p> <p>URL: <a href="https://www.youtube.com/watch?v=jRwi6h7Cq44">https://www.youtube.com/watch?v=jRwi6h7Cq44</a></p> <p>Demorgan theorem and Boolean algebra by Deepa R.M.</p> <p>URL: <a href="https://www.youtube.com/watch?v=3HbGsMKRQPI">https://www.youtube.com/watch?v=3HbGsMKRQPI</a></p> <p>MUX USING BASIC GATES by Deepa R.M.</p> <p>URL: <a href="https://www.youtube.com/watch?v=mQJOoTkJNs">https://www.youtube.com/watch?v=mQJOoTkJNs</a></p>
	VLSI Testing and verification (II Sem M.Tech)	<p>Creation of OER:</p> <p><a href="https://vlsitestng.wordpress.com/">https://vlsitestng.wordpress.com/</a></p>
	VLSI Design (I Sem M.Tech)	<p>Creation of OER:</p> <p><a href="https://digitalvlsidesign.wordpress.com/">https://digitalvlsidesign.wordpress.com/</a></p> <p>conduction of quiz:</p> <p><a href="http://www.edmodo.com">www.edmodo.com</a></p>
Journal Paper published on ICT	<p>Deepa, “<b>Open Education Resource:An Effective ICT Tool for Engineering Education</b> “ Journal of Engineering Education Transformations ,Volume 31 , No. 1, July 2017, ISSN 2349-2473, eISSN 2394-1707</p>	
Mrs. Ashwini K.B.	<p>Digital system Design: Assignment programs were simulated and verified in Xilinx spartan3 and same is forwarded to Email.</p>	